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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10006924	FILING DATE 12/04/2001	CLASS 514	SUBCLASS 327	GAU 2817	EXAMINER JACQUES WILLIOM
**APPLICANTS: Stockstad Troy; 330/255 Nguyen Khanh 07/515961 2/29/2000 6,353,363					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF 09/516,961 03/01/2000 PAT 6,316,469 * (*)Data provided by applicant is not consistent with PTO records.					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 60178-300402	
TITLE : Low voltage rail-to-rail CMOS output stage					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.0
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig. Drawn
		Crat. No.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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